

B 14152.3 GB

Declaration, Power Of Attorney and Petition

Page 1 of 2

WE (I) the undersigned inventor(s), hereby declare(s) that :

My residence, post office address and citizenship are as stated below next to my name,

We (I) believe that we are (I am) the original, first, and joint (sole) inventor(s) of the subject matter which is claimed and for which a patent is sought on the invention entitled

SELF ALIGNED GATE MIS TRANSISTOR AND ITS METHOD OF MANUFACTURE

the specification of which

☐ is attached hereto.

☐ was filed on

as Application Serial No.

and amended on

☒ was filed as PCT international application

Number PCT/FR03/50173

on DECEMBER 15, 2003

and was amended under PCT Article 19

on January 24, 2005

We (I) hereby state that we (I) have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

We (I) acknowledge the duty to disclose information known to be material to the patentability of this application as defined in Section 1.56 of Title 37 Code of Federal Regulations.

We (I) hereby claim foreign priority benefits under 35 U.S.C. § 119 (a)-(d) or § 365 (b) of any foreign application(s) for patent or inventor's certificate, or § 365 (a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed. Prior Foreign Application (s)

Application No.	Country	Day/month/Year	Priority Claimed	
02 15916	FRANCE	DECEMBER 16, 2002	<input checked="" type="checkbox"/> YES	<input type="checkbox"/> NO
_____	_____	_____	<input type="checkbox"/> YES	<input type="checkbox"/> NO
_____	_____	_____	<input type="checkbox"/> YES	<input type="checkbox"/> NO
_____	_____	_____	<input type="checkbox"/> YES	<input type="checkbox"/> NO

We (I) hereby claim the benefit under Title 35, United States Code, § 119 (e) of any United States provisional application(s) listed below.

(Application Number)

(Filing Date)

(Application Number)

(Filing Date)

We (I) hereby claim the benefit under 35 U.S.C. §120 of any United States application(s), listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in these prior United States application(s) in the manner provided by 35 U.S.C. § 112, I acknowledge the duty to disclose material information as defined in 37 CFR § 1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT International filing date of this application.

Application Serial No.

Filing Date

Status (pending, patented,
abandoned)

I hereby appoint : David B. Ritchie, Registration No. 31,562; Robert E. Krebs, Registration No. 25,885 ; Marc S. Hanish, Registration No. 42,626; John P. Schaub, Registration No. 42,125; Adrienne Yeung, Registration No. 44,000; Steven J. Robbins, Registration No. 40,299; Thierry K. Lo, Registration No. 49,097; William Samuel Niece, Registration No.: P47,824; J. Davis Gilmer, Registration No. 44,711 ; William E. Winters, Registration No. 42,232, Masako Ando, (37 C.F.R. §10.9) and John Klaas Uilkema, Registration No. 20,282 ; Becky L. Troutman, Registration No. 36,703 Hal J. Bohner, Registration No. 27,856 ; as attorneys of record with full power of substitution and revocation, to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith. If this application is assigned by me I agree and understand that the above-named attorneys will represent the assignee and not me.

Please send all correspondence and direct all telephone calls to : THELEN REID & PRIEST LLP

P.O. Box 640640, San Jose, CA 95164-0640.

Telephone : 408.292.5800 – Facsimile : 408.287-8040

We (I) undersigned declare that all statements made herein of my(our) own knowledge are true and that all statements made on information and belief are believed to be true and further that these statements were made with the knowledge that wilful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and such wilful false statements may jeopardize the validity of the application or any patent issuing therefrom.

DELEONIBUS Simon

NAME OF FIRST SOLE INVENTOR


Signature of Inventor

MAY 12, 2005
Date

Residence :

40 Allée des Coteaux
38640 CLAIR
FRANCE

Citizen of :

FRANCE

Post Office Address : The same as residence

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Simon Deleonibus

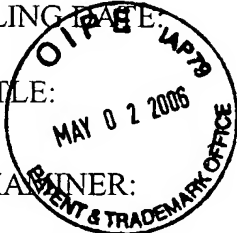
SERIAL NO.: 10/539,928

FILING DATE: June 16, 2005

TITLE: MIS Transistor with Self-Aligned Gate and Method for Making Same

EXAMINER: Unassigned

ART UNIT: Unassigned

**CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP: MISSING PARTS, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on the date printed below:

Date:

4/28/2006

Name:

Beatrice Bailey

Beatrice Bailey

MAILS TOP: MISSING PARTS
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

PATENT PRACTITIONERS TO BE MADE OF RECORD

Please recognize the following ten patent practitioners in the attached Declaration and Power of Attorney as being of record in the application or patent to which the power of attorney is directed:

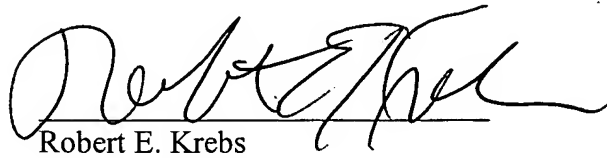
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The Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No. 50-1698.

Respectfully submitted,
THELEN REID & PRIEST LLP

Dated: April 28, 2006


Robert E. Krebs
Reg. No. 25,885

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